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## \*BIBDATASHEET\*

Bib Data Sheet

CONFIRMATION NO. 6686

SERIAL NUMBER 09/530,588	FILING DATE 05/05/2000  RULE	CLASS 257	GROUP ART UNIT 2814	ATTORNEY DOCKET NO. 2312-0866-2P
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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP98/04983 11/05/1998

*verified*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 9-319059 11/05/1997

JAPAN 10-207198 07/07/1998

*verified*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/06/2000

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	4	8	4
Verified and Acknowledged	Examiner's Signature <u>Thach TD</u> Initials				

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## TITLE

WIRING STRUCTURE OF SEMICONDUCTOR DEVICE, ELECTRODE, AND METHOD FOR FORMING THEM

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